**ECE 329**

**Digital Circuit Design II + Lab.**

**Fall 2018/2019**

**LAB # 8**

**26.11.2018**

**Objective:**

To learn about Resistor Transistor Logic (RTL).

1. **Understanding the Resistor Transistor Logic**
2. **Designing fundamental logic gates on Orcad.**
3. **Design the following task on ORCAD by Resistor Transistor Logic.**

**Tasks:**

1. Design an inverter and simulate.

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1. Design a Nand gate and simulate.

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1. Design a Nor gate and simulate.

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1. Extend your Nand gate to And gate and simulate.
2. Extend your Nor gate to Or gate and simulate